

RAJEEV GANDHI MEMORIAL COLLEGE OF ENGINEERING & TECHNOLOGY

(AUTONOMOUS)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

WEB OF SCIENCE (SCI/SCIE/ESCI) INDEXED JOURNALS

AY: 2020-2021

1. Shivendra Yadav, Anuj, Anju Gedam, Guru Prasad Mishra and Mohd. Aslam, “Linearity/Intermodulation Distortion Analysis of Tunneling and Thermionic Emission Mechanisms; Design Proposal and High Frequency Investigation”, Semiconductor Science and Technology, Aug 2020.[SCI]
2. Shivendra Yadav, Anju, and Sureshni Tirkey, “A Dielectric Modulated Biosensor for SARS-CoV-2”, IEEE Sensor Journal, Aug 2020. .[SCI]
3. Kethepalli Malliakarjuna, Beepar Abdul Raheem, Govindaraj Pathanadka, Sudhakar Mogappair Suriya kumar; “A Simple Shape Descriptor Merging Arithmetical WrapAround Technique with Absolute Localized Pixel Differences”, Wireless Personal Communications An International Journal, DOI 10.1007/s11277-020-07991-y, Journal ISSN 0929-6212 , Published online 28 Nov 2020.[SCI]
4. Nagaraja Kumar N, et al, Optimized Dual tree complex wavelet transform & fuzzy entropy for multimodal medical image fusion: A hybrid metaheuristic concept Journal of Mechanics in medical and biology vol.21.no.3, 2150024(2021), DOI , Published on feb 2021[SCI].
5. Jaya Chandra Prasad Talari, et al, Optimized Dual tree complex wavelet transform & fuzzy entropy for multimodal medical image fusion: A hybrid metaheuristic concept Journal of Mechanics in medical and biology vol.21.no.3, 2150024 (2021), DOI , Published on feb 2021[SCI].
6. Sanjib kalita, et al,” Comparative studies on the DC and RF performances of conventional HEMT and double quantum well heterostructure” Optical and Quantum Electronics (2021) 53:98 <https://doi.org/10.1007/s11082-021-02750-0> [SCI]
7. Bhaskar Awadhiya et al, “Design and Analysis of Improved Phase-Transition FinFET Utilizing Negative Capacitance” IEEE Transactions on Electron Devices Vol.68 No.2 Feb 2021.

ESCI:

1. Awadhiya, B., Kondekar, P.N., Yadav, S. *et al.* Insight into Threshold Voltage and Drain Induced Barrier Lowering in Negative Capacitance Field Effect Transistor. *Trans. Electr. Electron. Mater.* (Aug 2020). <https://doi.org/10.1007/s42341-020-00230-y>. [ESCI] Cited by 1
2. Venkataiah C, *et al.* Novel circuit modal of multiwalled CNT Bundle interconnects using Multi-valued Ternary logic, IETE Journal of research, DOI: 10.1080/3772063.2020.1864235, Published on 30 Dec 2020 [ESCI]